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APPLICANTS

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** FOREIGN APPLICATIONS ***** *NO* *****
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Phaw Bony TP</i> Examiner's Signature Initials				

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TITLE

Multi-stage output multiplexing circuits and methods for double data rate synchronous memory devices

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